FSC	132	(AC) × 2^{E} → (AC)

Byte Manipulation

IBP	133	*If* P – S > 0: P – S → P *If* P – S < 0: Y + 1 → Y; 36 – S → P			
ILDB	134	IBP, LDB	LDB	135	BYTE IN ((E)) → (AC)
IDPB	136	IBP, DPB	DPB	137	BYTE IN (AC) → BYTE IN ((E))

Floating Point Arithmetic

FAD	140	(AC) + (E) → (AC)	FADR	144	(AC) + (E) → (AC)
FADL	141	(AC) + (E) → (AC,AC+1)	FADRL	145	(AC) + (E) → (AC,AC+1)
FADM	142	(AC) + (E) → (E)	FADRM	146	(AC) + (E) → (E)
FADB	143	(AC) + (E) → (AC)(E)	FADRB	147	(AC) + (E) → (AC)(E)
FSB	150	(AC) – (E) → (AC)	FSBR	154	(AC) – (E) → (AC)
FSBL	151	(AC) – (E) → (AC,AC+1)	FSBRL	155	(AC) – (E) → (AC,AC+1)
FSBM	152	(AC) – (E) → (E)	FSBRM	156	(AC) – (E) → (E)
FSBB	153	(AC) – (E) → (AC)(E)	FSBRB	157	(AC) – (E) → (AC)(E)
FMP	160	(AC) × (E) → (AC)	FMPR	164	(AC) × (E) → (AC)
FMPL	161	(AC) × (E) → (AC,AC+1)	FMPRL	165	(AC) × (E) → (AC,AC+1)
FMPM	162	(AC) × (E) → (E)	FMPRM	166	(AC) × (E) → (E)
FMPB	163	(AC) × (E) → (AC)(E)	FMPRB	167	(AC) × (E) → (AC)(E)
FDV	170	(AC) / (E) → (AC)	FDVR	174	(AC) / (E) → (AC)
FDVL	171	(AC) / (E) → (AC) REMAINDER→ (AC+1)	FDVRL	175	(AC) / (E) → (AC) REMAINDER→ (AC+1)
FDVM	172	(AC) / (E) → (E)	FDVRM	176	(AC) / (E) → (E)
FDVB	173	(AC) / (E) → (AC)(E)	FDVRB	177	(AC) / (E) → (AC)(E)

Full Word Data Transmission

MOVE	200	(E) → (AC)	MOVS	204	$(E)_S$ → (AC)
MOVEI	201	0,E → (AC)	MOVSI	205	E,0 → (AC)
MOVEM	202	(AC) → (E)	MOVSM	206	$(AC)_S$ → (E)
MOVES	203	*If* AC ≠ 0: (E) → (AC)	MOVSS	207	$(E)_S$ → (E) *If* AC ≠ 0: (E) → (AC)
MOVN	210	– (E) → (AC)	MOVM	214	|(E)| → (AC)
MOVNI	211	– [0,E] → (AC)	MOVMI	215	0,E → (AC)
MOVNM	212	– (AC) → (E)	MOVMM	216	|(AC)| → (E)
MOVNS	213	– (E) → (E) *If* AC ≠ 0: (E) → (AC)	MOVMS	217	|(E)| → (E) *If* AC ≠ 0: (E) → (AC)

Fixed Point Multiply-Divide

IMUL	220	(AC) × (E) → (AC)	MUL	224	(AC) × (E) → (AC,AC+1)
IMULI	221	(AC) × 0,E → (AC)	MULI	225	(AC) × 0,E → (AC,AC+1)
IMULM	222	(AC) × (E) → (E)	MULM	226	(AC) × (E) → (E)
IMULB	223	(AC) × (E) → (AC)(E)	MULB	227	(AC) × (E) → (AC,AC+1)(E)
IDIV	230	(AC) / (E) → (AC) REMAINDER→ (AC+1)	DIV	234	(AC,AC+1) / (E) → (AC) REMAINDER→ (AC+1)
IDIVI	231	(AC) / 0,E → (AC) REMAINDER→ (AC+1)	DIVI	235	(AC,AC+1) / 0,E → (AC) REMAINDER→ (AC+1)
IDIVM	232	(AC) / (E) → (E)	DIVM	236	(AC,AC+1) / (E) → (E)
IDIVB	233	(AC) / (E) → (AC)(E) REMAINDER→ (AC+1)	DIVB	237	(AC,AC+1) / (E) → (AC)(E) REMAINDER→ (AC+1)

Shift and Rotate

ASH	240	(AC) × 2^E → (AC)	ASHC	244	(AC,AC+1) × 2^E → (AC,AC+1)
ROT	241	Rotate (AC) E places	ROTC	245	Rotate (AC,AC+1) E places
LSH	242	Shift (AC) E places	LSHC	246	Shift (AC,AC+1) E places

Miscellaneous

EXCH	250	(AC) ↔ (E)
BLT	251	Move E – $(AC)_R$+1 words starting with $((AC)_L)$ → $((AC)_R)$
AOBJP	252	(AC) + 1000001 → (AC); *If* (AC) ≥ 0: E → (PC)
AOBJN	253	(AC) + 1000001 → (AC); *If* (AC) < 0: E → (PC)
JRST	254	E → (PC) [*If* AC ≠ 0, reset processor state according to AC]
JFCL	255	*If* AC ∧ FLAGS ≠ 0: E → (PC); ~AC ∧ FLAGS → FLAGS
XCT	256	Execute (E)

Pushdown List and Jumps

PUSHJ	260	(AC) + 1000001 → (AC); FLAGS,(PC) → $((AC)_R)$; E → (PC)
PUSH	261	(AC) + 1000001 → (AC); (E) → $((AC)_R)$
POP	262	$((AC)_R)$ → (E); (AC) – 1000001 → (AC)
POPJ	263	$((AC)_R)_R$→ (PC); (AC) – 1000001 → (AC)
JSR	264	FLAGS,(PC) → (E); E + 1 → (PC)
JSP	265	FLAGS,(PC) → (AC); E → (PC)
JSA	266	(AC) → (E); E,(PC) → (AC); E + 1 → (PC)
JRA	267	E → (PC); $((AC)_L)$ → (AC)

Fixed Point Add-Subtract

ADD	270	(AC) + (E) → (AC)	SUB	274	(AC) – (E) → (AC)
ADDI	271	(AC) + 0,E → (AC)	SUBI	275	(AC) – 0,E → (AC)
ADDM	272	(AC) + (E) → (E)	SUBM	276	(AC) – (E) → (E)
ADDB	273	(AC) + (E) → (AC)(E)	SUBB	277	(AC) – (E) → (AC)(E)

Arithmetic Testing

CAI	300	No-op	CAM	310	No-op
CAIL	301	*If* (AC) < E: *skip*	CAML	311	*If* (AC) < (E): *skip*
CAIE	302	*If* (AC) = E: *skip*	CAME	312	*If* (AC) = (E): *skip*
CAILE	303	*If* (AC) ≤ E: *skip*	CAMLE	313	*If* (AC) ≤ (E): *skip*
CAIA	304	*skip*	CAMA	314	*skip*
CAIGE	305	*If* (AC) ≥ E: *skip*	CAMGE	315	*If* (AC) ≥ (E): *skip*
CAIN	306	*If* (AC) ≠ E: *skip*	CAMN	316	*If* (AC) ≠ (E): *skip*
CAIG	307	*If* (AC) > E: *skip*	CAMG	317	*If* (AC) > (E): *skip*
JUMP	320	No-op	SKIP	330	*If* AC ≠ 0: (E) → (AC)
JUMPL	321	*If* (AC) < 0: E → (PC)	SKIPL	331	*If* AC ≠ 0: (E) → (AC) *If* (E) < 0: *skip*
JUMPE	322	*If* (AC) = 0: E → (PC)	SKIPE	332	*If* AC ≠ 0: (E) → (AC) *If* (E) = 0: *skip*
JUMPLE	323	*If* (AC) ≤ 0: E → (PC)	SKIPLE	333	*If* AC ≠ 0: (E) → (AC) *If* (E) ≤ 0: *skip*
JUMPA	324	E → (PC)	SKIPA	334	*If* AC ≠ 0: (E) → (AC) *skip*
JUMPGE	325	*If* (AC) ≥ 0: E → (PC)	SKIPGE	335	*If* AC ≠ 0: (E) → (AC) *If* (E) ≥ 0: *skip*
JUMPN	326	*If* (AC) ≠ 0: E → (PC)	SKIPN	336	*If* AC ≠ 0: (E) → (AC) *If* (E) ≠ 0: *skip*
JUMPG	327	*If* (AC) > 0: E → (PC)	SKIPG	337	*If* AC > 0: (E) → (AC) *If* (E) > 0: *skip*
AOJ	340	(AC) + 1 → (AC)	SOJ	360	(AC) - 1 → (AC)
AOJL	341	(AC) + 1 → (AC) *If* (AC) < 0: E → (PC)	SOJL	361	(AC) - 1 → (AC) *If* (AC) < 0: E → (PC)
AOJE	342	(AC) + 1 → (AC) *If* (AC) = 0: E → (PC)	SOJE	362	(AC) - 1 → (AC) *If* (AC) = 0: E → (PC)
AOJLE	343	(AC) + 1 → (AC) *If* (AC) ≤ 0: E → (PC)	SOJLE	363	(AC) - 1 → (AC) *If* (AC) ≤ 0: E → (PC)
AOJA	344	(AC) + 1 → (AC) E → (PC)	SOJA	364	(AC) - 1 → (AC) E → (PC)
AOJGE	345	(AC) + 1 → (AC) *If* (AC) ≥ 0: E → (PC)	SOJGE	365	(AC) - 1 → (AC) *If* (AC) ≥ 0: E → (PC)
AOJN	346	(AC) + 1 → (AC) *If* (AC) ≠ 0: E → (PC)	SOJN	366	(AC) - 1 → (AC) *If* (AC) ≠ 0: E → (PC)
AOJG	347	(AC) + 1 → (AC) *If* (AC) ≥ 0: E → (PC)	SOJG	367	(AC) - 1 → (AC) *If* (AC) ≥ 0: E → (PC)

AOS	350	(E) + 1 → (E) *If* AC ≠ 0: (E) → (AC)
AOSL	351	(E) + 1 → (E) *If* AC ≠ 0: (E) → (AC) *If* (E) < 0: *skip*
AOSE	352	(E) + 1 → (E) *If* AC ≠ 0: (E) → (AC) *If* (E) = 0: *skip*
AOSLE	353	(E) + 1 → (E) *If* AC ≠ 0: (E) → (AC) *If* (E) ≤ 0: *skip*
AOSA	354	(E) + 1 → (E) *If* AC ≠ 0: (E) → (AC) *skip*
AOSGE	355	(E) + 1 → (E) *If* AC ≠ 0: (E) → (AC) *If* (E) ≥ 0: *skip*
AOSN	356	(E) + 1 → (E) *If* AC ≠ 0: (E) → (AC) *If* (E) ≠ 0: *skip*
AOSG	357	(E) + 1 → (E) *If* AC ≠ 0: (E) → (AC) *If* (E) > 0: *skip*

SOS	370	(E) - 1 → (E) *If* AC ≠ 0: (E) → (AC)
SOSL	371	(E) - 1 → (E) *If* AC ≠ 0: (E) → (AC) *If* (E) < 0: *skip*
SOSE	372	(E) - 1 → (E) *If* AC ≠ 0: (E) → (AC) *If* (E) = 0: *skip*
SOSLE	373	(E) - 1 → (E) *If* AC ≠ 0: (E) → (AC) *If* (E) ≤ 0: *skip*
SOSA	374	(E) - 1 → (E) *If* AC ≠ 0: (E) → (AC) *skip*
SOSGE	375	(E) - 1 → (E) *If* AC ≠ 0: (E) → (AC) *If* (E) ≥ 0: *skip*
SOSN	376	(E) - 1 → (E) *If* AC ≠ 0: (E) → (AC) *If* (E) ≠ 0: *skip*
SOSG	377	(E) - 1 → (E) *If* AC ≠ 0: (E) → (AC) *If* (E) > 0: *skip*

Boolean

SETZ	400	0 → (AC)	AND	404	(AC) ∧ (E) → (AC)
SETZI	401	0 → (AC)	ANDI	405	(AC) ∧ 0,E → (AC)
SETZM	402	0 → (E)	ANDM	406	(AC) ∧ (E) → (E)
SETZB	403	0 → (AC)(E)	ANDB	407	(AC) ∧ (E) → (AC)(E)
ANDCA	410	~(AC) ∧ (E) → (AC)	SETM	414	(E) → (AC)
ANDCAI	411	~(AC) ∧ 0,E → (AC)	SETMI	415	0,E → (AC)
ANDCAM	412	~(AC) ∧ (E) → (E)	SETMM	416	(E) → (E) [no-op]
ANDCAB	413	~(AC) ∧ (E) → (AC)(E)	SETMB	417	(E) → (AC)(E)
ANDCM	420	(AC) ∧ ~(E) → (AC)	SETA	424	(AC) → (AC) [no-op]
ANDCMI	421	(AC) ∧ ~[0,E] → (AC)	SETAI	425	(AC) → (AC) [no-op]
ANDCMM	422	(AC) ∧ ~(E) → (E)	SETAM	426	(AC) → (E)
ANDCMB	423	(AC) ∧ ~(E) → (AC)(E)	SETAB	427	(AC) → (AC)(E)
XOR	430	(AC) ⊻ (E) → (AC)	IOR	434	(AC) ∨ (E) → (AC)
XORI	431	(AC) ⊻ 0,E → (AC)	IORI	435	(AC) ∨ 0,E → (AC)
XORM	432	(AC) ⊻ (E) → (E)	IORM	436	(AC) ∨ (E) → (E)
XORB	433	(AC) ⊻ (E) → (AC)(E)	IORB	437	(AC) ∨ (E) → (AC)(E)
ANDCB	440	~(AC) ∧ ~(E) → (AC)	EQV	444	~[(AC) ⊻ (E)] → (AC)
ANDCBI	441	~(AC) ∧ ~[0,E] → (AC)	EQVI	445	~[(AC) ⊻ 0,E] → (AC)
ANDCBM	442	~(AC) ∧ ~(E) → (E)	EQVM	446	~[(AC) ⊻ (E)] → (E)
ANDCBB	443	~(AC) ∧ ~(E) → (AC)(E)	EQVB	447	~[(AC) ⊻ (E)] → (AC)(E)
SETCA	450	~(AC) → (AC)	ORCA	454	~(AC) ∨ (E) → (AC)
SETCAI	451	~(AC) → (AC)	ORCAI	455	~(AC) ∨ 0,E → (AC)
SETCAM	452	~(AC) → (E)	ORCAM	456	~(AC) ∨ (E) → (E)
SETCAB	453	~(AC) → (AC)(E)	ORCAB	457	~(AC) ∨ (E) → (AC)(E)
SETCM	460	~(E) → (AC)	ORCM	464	(AC) ∨ ~(E) → (AC)
SETCMI	461	~[0,E] → (AC)	ORCMI	465	(AC) ∨ ~[0,E] → (AC)
SETCMM	462	~(E) → (E)	ORCMM	466	(AC) ∨ ~(E) → (E)
SETCMB	463	~(E) → (AC)(E)	ORCMB	467	(AC) ∨ ~(E) → (AC)(E)
ORCB	470	~(AC) ∨ ~(E) → (AC)	SETO	474	*777777777777* → (AC)
ORCBI	471	~(AC) ∨ ~[0,E] → (AC)	SETOI	475	*777777777777* → (AC)
ORCBM	472	~(AC) ∨ ~(E) → (E)	SETOM	476	*777777777777* → (E)
ORCBB	473	~(AC) ∨ ~(E) → (AC)(E)	SETOB	477	*777777777777* → (AC)(E)

Half Word Data Transmission

HLL	500	$(E)_L \rightarrow (AC)_L$	HRL	504	$(E)_R \rightarrow (AC)_L$
HLLI	501	$0 \rightarrow (AC)_L$	HRLI	505	$E \rightarrow (AC)_L$
HLLM	502	$(AC)_L \rightarrow (E)_L$	HRLM	506	$(AC)_R \rightarrow (E)_L$
HLLS	503	*If* AC ≠ 0: (E) → (AC)	HRLS	507	$(E)_R \rightarrow (E)_L$ *If* AC ≠ 0: (E) → (AC)
HLLZ	510	$(E)_L,0 \rightarrow (AC)$	HRLZ	514	$(E)_R,0 \rightarrow (AC)$
HLLZI	511	$0 \rightarrow (AC)$	HRLZI	515	$E,0 \rightarrow (AC)$
HLLZM	512	$(AC)_L,0 \rightarrow (E)$	HRLZM	516	$(AC)_R,0 \rightarrow (E)$
HLLZS	513	$0 \rightarrow (E)_R$ *If* AC ≠ 0: (E) → (AC)	HRLZS	517	$(E)_R,0 \rightarrow (E)$ *If* AC ≠ 0: (E) → (AC)
HLLO	520	$(E)_L,777777 \rightarrow (AC)$	HRLO	524	$(E)_R,777777 \rightarrow (AC)$
HLLOI	521	$0,777777 \rightarrow (AC)$	HRLOI	525	$E,777777 \rightarrow (AC)$
HLLOM	522	$(AC)_L,777777 \rightarrow (E)$	HRLOM	526	$(AC)_R,777777 \rightarrow (E)$
HLLOS	523	$777777 \rightarrow (E)_R$ *If* AC ≠ 0: (E) → (AC)	HRLOS	527	$(E)_R,777777 \rightarrow (E)$ *If* AC ≠ 0: (E) → (AC)
HLLE	530	$(E)_L,[(E)_0 \times 777777] \rightarrow (AC)$	HRLE	534	$(E)_R,[(E)_{18} \times 777777] \rightarrow (AC)$
HLLEI	531	$0 \rightarrow (AC)$	HRLEI	535	$E,[E_{18} \times 777777] \rightarrow (AC)$
HLLEM	532	$(AC)_L,[(AC)_0 \times 777777] \rightarrow (E)$	HRLEM	536	$(AC)_R,[(AC)_{18} \times 777777] \rightarrow (E)$
HLLES	533	$(E)_0 \times 777777 \rightarrow (E)_R$ *If* AC ≠ 0: (E) → (AC)	HRLES	537	$(E)_R,[(E)_{18} \times 777777] \rightarrow (E)$ *If* AC ≠ 0: (E) → (AC)
HRR	540	$(E)_R \rightarrow (AC)_R$	HLR	544	$(E)_L \rightarrow (AC)_R$
HRRI	541	$E \rightarrow (AC)_R$	HLRI	545	$0 \rightarrow (AC)_R$
HRRM	542	$(AC)_R \rightarrow (E)_R$	HLRM	546	$(AC)_L \rightarrow (E)_R$
HRRS	543	*If* AC ≠ 0: (E) → (AC)	HLRS	547	$(E)_L \rightarrow (E)_R$ *If* AC ≠ 0: (E) → (AC)
HRRZ	550	$0,(E)_R \rightarrow (AC)$	HLRZ	554	$0,(E)_L \rightarrow (AC)$
HRRZI	551	$0,E \rightarrow (AC)$	HLRZI	555	$0 \rightarrow (AC)$
HRRZM	552	$0,(AC)_R \rightarrow (E)$	HLRZM	556	$0,(AC)_L \rightarrow (E)$
HRRZS	553	$0 \rightarrow (E)_L$ *If* AC ≠ 0: (E) → (AC)	HLRZS	557	$0,(E)_L \rightarrow (E)$ *If* AC ≠ 0: (E) → (AC)
HRRO	560	$777777,(E)_R \rightarrow (AC)$	HLRO	564	$777777,(E)_L \rightarrow (AC)$
HRROI	561	$777777,E \rightarrow (AC)$	HLROI	565	$777777,0 \rightarrow (AC)$
HRROM	562	$777777,(AC)_R \rightarrow (E)$	HLROM	566	$777777,(AC)_L \rightarrow (E)$
HRROS	563	$777777 \rightarrow (E)_L$ *If* AC ≠ 0: (E) → (AC)	HLROS	567	$777777,(E)_L \rightarrow (E)$ *If* AC ≠ 0: (E) → (AC)
HRRE	570	$[(E)_{18} \times 777777],(E)_R \rightarrow (AC)$	HLRE	574	$[(E)_0 \times 777777],(E)_L \rightarrow (AC)$
HRREI	571	$[E_{18} \times 777777],E \rightarrow (AC)$	HLREI	575	$0 \rightarrow (AC)$
HRREM	572	$[(AC)_{18} \times 777777],(AC)_R \rightarrow (E)$	HLREM	576	$[(AC)_0 \times 777777],(AC)_L \rightarrow (E)$
HRRES	573	$(E)_{18} \times 777777 \rightarrow (E)_L$ *If* AC ≠ 0: (E) → (AC)	HLRES	577	$[(E)_0 \times 777777],(E)_L \rightarrow (E)$ *If* AC ≠ 0: (E) → (AC)

Logical Testing and Manipulation

TRN	600	No-op	TLN	601	No-op
TRNE	602	*If* $(AC)_R \wedge E = 0$: *skip*	TLNE	603	*If* $(AC)_L \wedge E = 0$: *skip*
TRNA	604	*skip*	TLNA	605	*skip*
TRNN	606	*If* $(AC)_R \wedge E \neq 0$: *skip*	TLNN	607	*If* $(AC)_L \wedge E \neq 0$: *skip*
TDN	610	No-op	TSN	611	No-op
TDNE	612	*If* $(AC) \wedge (E) = 0$: *skip*	TSNE	613	*If* $(AC) \wedge (E)_S = 0$: *skip*
TDNA	614	*skip*	TSNA	615	*skip*
TDNN	616	*If* $(AC) \wedge (E) \neq 0$: *skip*	TSNN	617	*If* $(AC) \wedge (E)_S \neq 0$: *skip*
TRZ	620	$(AC)_R \wedge \sim E \rightarrow (AC)_R$	TLZ	621	$(AC)_L \wedge \sim E \rightarrow (AC)_L$
TRZE	622	*If* $(AC)_R \wedge E = 0$: *skip* $(AC)_R \wedge \sim E \rightarrow (AC)_R$	TLZE	623	*If* $(AC)_L \wedge E = 0$: *skip* $(AC)_L \wedge \sim E \rightarrow (AC)_L$
TRZA	624	$(AC)_R \wedge \sim E \rightarrow (AC)_R$ *skip*	TLZA	625	$(AC)_L \wedge \sim E \rightarrow (AC)_L$ *skip*
TRZN	626	*If* $(AC)_R \wedge E \neq 0$: *skip* $(AC)_R \wedge \sim E \rightarrow (AC)_R$	TLZN	627	*If* $(AC)_L \wedge E \neq 0$: *skip* $(AC)_L \wedge \sim E \rightarrow (AC)_L$
TDZ	630	$(AC) \wedge \sim(E) \rightarrow (AC)$	TSZ	631	$(AC) \wedge \sim(E)_S \rightarrow (AC)$
TDZE	632	*If* $(AC) \wedge (E) = 0$: *skip* $(AC) \wedge \sim(E) \rightarrow (AC)$	TSZE	633	*If* $(AC) \wedge (E)_S = 0$: *skip* $(AC) \wedge \sim(E)_S \rightarrow (AC)$
TDZA	634	$(AC) \wedge \sim(E) \rightarrow (AC)$ *skip*	TSZA	635	$(AC) \wedge \sim(E)_S \rightarrow (AC)$ *skip*
TDZN	636	*If* $(AC) \wedge (E) \neq 0$: *skip* $(AC) \wedge \sim(E) \rightarrow (AC)$	TSZN	637	*If* $(AC) \wedge (E)_S \neq 0$: *skip* $(AC) \wedge \sim(E)_S \rightarrow (AC)$
TRC	640	$(AC)_R \veebar E \rightarrow (AC)_R$	TLC	641	$(AC)_L \veebar E \rightarrow (AC)_L$
TRCE	642	*If* $(AC)_R \wedge E = 0$: *skip* $(AC)_R \veebar E \rightarrow (AC)_R$	TLCE	643	*If* $(AC)_L \wedge E = 0$: *skip* $(AC)_L \veebar E \rightarrow (AC)_L$
TRCA	644	$(AC)_R \veebar E \rightarrow (AC)_R$ *skip*	TLCA	645	$(AC)_L \veebar E \rightarrow (AC)_L$ *skip*
TRCN	646	*If* $(AC)_R \wedge E \neq 0$: *skip* $(AC)_R \veebar E \rightarrow (AC)_R$	TLCN	647	*If* $(AC)_L \wedge E \neq 0$: *skip* $(AC)_L \veebar E \rightarrow (AC)_L$
TDC	650	$(AC) \veebar (E) \rightarrow (AC)$	TSC	651	$(AC) \veebar (E)_S \rightarrow (AC)$
TDCE	652	*If* $(AC) \wedge (E) = 0$: *skip* $(AC) \veebar (E) \rightarrow (AC)$	TSCE	653	*If* $(AC) \wedge (E)_S = 0$: *skip* $(AC) \veebar (E)_S \rightarrow (AC)$
TDCA	654	$(AC) \veebar (E) \rightarrow (AC)$ *skip*	TSCA	655	$(AC) \veebar (E)_S \rightarrow (AC)$ *skip*
TDCN	656	*If* $(AC) \wedge (E) \neq 0$: *skip* $(AC) \veebar (E) \rightarrow (AC)$	TSCN	657	*If* $(AC) \wedge (E)_S \neq 0$: *skip* $(AC) \veebar (E)_S \rightarrow (AC)$
TRO	660	$(AC)_R \vee E \rightarrow (AC)_R$	TLO	661	$(AC)_L \vee E \rightarrow (AC)_R$
TROE	662	*If* $(AC)_R \wedge E = 0$: *skip* $(AC)_R \vee E \rightarrow (AC)_R$	TLOE	663	*If* $(AC)_L \wedge E = 0$: *skip* $(AC)_L \vee E \rightarrow (AC)_R$
TROA	664	$(AC)_R \vee E \rightarrow (AC)_R$ *skip*	TLOA	665	$(AC)_L \vee E \rightarrow (AC)_R$ *skip*
TRON	666	*If* $(AC)_R \wedge E \neq 0$: *skip* $(AC)_R \vee E \rightarrow (AC)_R$	TLON	667	*If* $(AC)_L \wedge E \neq 0$: *skip* $(AC)_L \vee E \rightarrow (AC)_R$
TDO	670	$(AC) \vee (E) \rightarrow (AC)$	TSO	671	$(AC) \vee (E)_S \rightarrow (AC)$
TDOE	672	*If* $(AC) \wedge (E) = 0$: *skip* $(AC) \vee (E) \rightarrow (AC)$	TSOE	673	*If* $(AC) \wedge (E)_S = 0$: *skip* $(AC) \vee (E)_S \rightarrow (AC)$
TDOA	674	$(AC) \vee (E) \rightarrow (AC)$ *skip*	TSOA	675	$(AC) \vee (E)_S \rightarrow (AC)$ *skip*
TDON	676	*If* $(AC) \wedge (E) \neq 0$: *skip* $(AC) \vee (E) \rightarrow (AC)$	TSON	677	*If* $(AC) \wedge (E)_S \neq 0$: *skip* $(AC) \vee (E)_S \rightarrow (AC)$