



read in
 clear alarm
 start over
 start at 40
 stop
 start clock
 restart
 order by order
 examine

9 PBs

stop on si-l
 stop on sel. pulse

2 SWs

5x FFS reset sel + PB/TS

2 SWs, 2 PBs

alarms:
 arithmetic
 divide
 check
 program
 inactivity
 master
 continuous

7 LEDs